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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

**Title of
Invention**

SYSTEM AND METHOD FOR IMPLEMENTING A
MICRO-STEPPING DELAY CHAIN FOR A DELAY LOCKED LOOP

Application Number : 10/708,287

Confirmation Number:

First Named Applicant: Harold Pilo

Attorney Docket Number: BUR920030199US1

Art Unit:

Examiner:

Search string: (5287025 or 5223755 or 5422835 or 5604775 or 5854615 or 6031429 or 6100735
or 6101197 or 6255880 or 6316976 or 6125157).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
MT	1	5287025	1994-02-15	Nishimichi		307	603
MT	2	5223755	1993-06-29	Richley		307	603
MT	3	5422835	1995-06-06	Houle et al.		364	703
MT	4	5604775	1997-02-18	Saitoh et al		375	376
MT	5	5854615	1998-12-29	Hush		345	99
MT	6	6031429	2000-02-29	Shen		331	17
MT	7	6100735	2000-08-08	Lu		327	158
MT	8	6101197	2000-08-08	Keeth et al.		370	517
MT	9	6255880	2001-07-03	Nguyen		327	277
MT	10	6316976	2001-11-13	Miller, Jr. et al.		327	156
MT	11	6125157	2000-09-26	Donnelly, et al.		375	371

Signature

Examiner Name	Date
	7/28/05